Low Cost Interdigital BST Varactors for Tunable Microwave Applications

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Abstract—In this paper, a simple and inexpensive technique for the fabrication of barium strontium titanate (BST) varactors is presented. BST films were deposited using RF magnetron sputtering on platinum coated silicon substrates. The advantages of silicon include low cost and ease of integration with existing fabrication processes. The microstructural properties of the BST layer were examined with scanning electron microscopy (SEM), x-ray diffraction (XRD) and x-ray photoelectron spectroscopy (XPS) techniques. Gold top layer electrodes were deposited using DC sputtering and patterned with a conventional photolithographic process. The S-parameters of the varactor were measured from 40 MHz to 20 GHz on a vector network analyser. From this data, the capacitance, tunability and Q-factor of the device were extracted. The BST-based varactors show potential for integration into electronically tunable microwave circuits.

I. INTRODUCTION

There has been considerable research into the properties of $\text{Ba}_{x}\text{Sr}_{1-x}\text{TiO}_3$ (BST) thin films in recent years. Potential applications of BST include high density dynamic random access memory (DRAM) [1], ferroelectric random access memory (FERAM) [2] and tunable microwave components [3]. The high, field dependent permittivity and low loss tangent of BST make it attractive as a dielectric material for electronically controlled microwave devices such as filters, phase shifters and matching networks [4]. Strong growth in RF-based applications over the past decade or so suggests a large potential market for these devices.

In order for BST-based microwave components to become commercially successful, it is important to minimise fabrication costs. Expensive, single-crystal substrates such as sapphire [5], MgO and LaAlO$_3$ [6] have often been used as substrates for the deposition of BST thin films. These substrates have low microwave losses, and provide a good lattice match to BST. Many efforts have also been made to fabricate BST films on Pt/Si substrates [7], [3]. However, there are outstanding problems with the Pt bottom layer, which tends to form hillocks or cracks at the high temperatures used for BST deposition. In this paper, RF magnetron sputtering was used to deposit $\text{Ba}_{0.5}\text{Sr}_{0.5}\text{TiO}_3$ films on low-cost platinised Si substrates. Tunable BST-based devices were produced, however a problem with cracking in the Pt layer was encountered and is the subject of ongoing work. The process that has been developed could easily be transferred to high-resistivity Si, since most microwave circuit applications require low-loss transmission lines for the connection of individual devices.

II. INTERDIGITAL VARACTOR FABRICATION

Low resistivity 20 $\Omega \cdot \text{cm}$, 0.5mm thick Si(100) wafers were used as the substrate material. A 500nm film of SiO$_2$ was deposited using RF magnetron sputtering at a working pressure of 10 mTorr and a substrate temperature of 120 $^\circ$C. The oxide layer serves as a diffusion barrier and inhibits silicide formation [8]. A Balzers BAK600 e-beam evaporation system was used to deposit 200nm layers of TiO$_2$ and Pt successively. The TiO$_2$ layer promotes adhesion of the Pt ground plane to the substrate. Because there was no radiation shield on the available e-beam deposition system, the TiO$_2$ and Pt layers were deposited at room temperature to prevent damage to the cryopump.

The BST layer was deposited at high temperature to promote the formation of a polycrystalline film. A resistive heater was used to heat the substrate to 625$^\circ$C during BST deposition.
The films were on-axis magnetron sputtered from a 100 mm diameter target supplied by Williams Advanced Materials. The base pressure was 1 × 10⁻⁵ Torr and the working pressure was 5 mTorr and 20 mTorr in a Ar/O₂ 9:1 atmosphere for different samples. A target to substrate distance of 60 mm was set for all depositions. No post-deposition anneal was performed. Finally, a thin layer of gold was DC sputtered on top of the BST film. A cross section of the layers comprising the device is shown in Fig. 1.

Interdigital top electrodes were patterned in the gold film using AZ5206E photoresist and a cyanide wet etch. In order that the high frequency properties of the film could be measured on a wafer probe station, the spacing of the contact pads had to match the wafer probes. PicoProbe 40A probes with a spacing of 200 µm were available. Because of the ground-signal-ground (GSG) configuration of the probes, each device consists of two sets of interdigital electrodes. The DC bias is applied to the central contact pad, and the outer pads serve as ground contacts. Many different interdigital devices were patterned on the BST sample, in order to realise a range of capacitances. The number of finger pairs per device ranged from 4 to 6, the finger length from 50 to 100 µm, and the finger spacing ranged from 5 to 15 µm. The layout of an example device is shown in Fig. 2.

The composition of a BST film sputtered at 5 mTorr was studied using a MicroLab 310-F instrument for x-ray photoelectron spectroscopy (XPS) analysis. Measurements were made with non-monochromated Al Kα radiation. Aside from the standard carbon signature inherent to XPS measurements, there was no significant trace of contaminants in the film. The carbon peak at 284.5 eV is useful in compensating for sample charging during the XPS analysis. Using a calibration standard of BST powder from the sputtering target, it should be possible to determine an accurate elemental composition of the BST film. However, this was not available when the XPS measurement was performed; consequently, there is some uncertainty in the composition analysis. The XPS survey in Fig. 4 was taken after a 5 min sputter to remove the surface layer of the BST film containing the carbon signature. Composition results from the survey are given in Table I. The results suggest that the film is oxygen deficient, which may be due to the low Ar/O₂ atmosphere (5 mTorr) used for the deposition of the film. It has been reported elsewhere that oxygen vacancies contribute to dielectric loss in BST films [11]. Efforts are now underway to characterise the composition of the BST film as a function of the sputtering gas pressure.

X-ray diffraction was used to study the crystal structure of a BST film sputtered at 5 mTorr. The results, shown in Fig. 5, were obtained using Cu Kα radiation with a detector slit width...
TABLE I
COMPOSITION RESULTS

<table>
<thead>
<tr>
<th>Element</th>
<th>Ideal At. %</th>
<th>XPS Meas. At. %</th>
</tr>
</thead>
<tbody>
<tr>
<td>Ba</td>
<td>10</td>
<td>13.8</td>
</tr>
<tr>
<td>Sr</td>
<td>10</td>
<td>11.6</td>
</tr>
<tr>
<td>Ti</td>
<td>20</td>
<td>20.4</td>
</tr>
<tr>
<td>O</td>
<td>60</td>
<td>54.2</td>
</tr>
</tbody>
</table>

Fig. 5. XRD pattern of 5 mTorr magnetron sputtered film showing (110) BST orientation.

of 0.5 mm and a slit height of 2 mm. The BST film is weakly oriented, with a (110) peak at 31.95°. This peak suggests a lattice parameter of 3.96 Å, 3% larger than the bulk BST value of 3.9471 Å [12].

Using an Anritsu 37369A vector network analyser (VNA), the two–port S parameters of the interdigital structures were measured from 40 MHz to 20 GHz. This measurement was performed on a BST film sputtered at 20 mTorr. A calibration wafer supplied with the 200 µm GSG probes was used to calibrate to the reference plane of the probe tips. For extraction of the dielectric parameters, the device with dimensions given in Fig. 2 was measured. Figure 6 shows the S-parameters of this device, at 0 V and 10 V bias. The change in $S_{21}$ with bias voltage demonstrates the phase-shifting property of the varactor. The capacitance of the device is quite large, and the impedance is very close to the short circuit point on the Smith chart beyond 1 GHz. Therefore, the device is useful as a tunable circuit element only at frequencies below 1 GHz. The relative permittivity $\varepsilon_r$ of the dielectric was determined using a full wave electromagnetic simulation in Ansoft HFSS.

TABLE II
EQUIVALENT CIRCUIT MODEL PARAMETERS

<table>
<thead>
<tr>
<th>DC Bias</th>
<th>$C$ [pF]</th>
<th>$R$ [$\Omega$]</th>
<th>$Q$ at 40 MHz</th>
</tr>
</thead>
<tbody>
<tr>
<td>0 V</td>
<td>198.8</td>
<td>0.72</td>
<td>27.8</td>
</tr>
<tr>
<td>10 V</td>
<td>156.6</td>
<td>0.73</td>
<td>35.0</td>
</tr>
</tbody>
</table>

In order to extract the capacitance and loss tangent of the device, the measured S-parameters were imported into Agilent Advanced Design System (ADS) 2004A. A simple parallel RC circuit was used to model the varactor over the frequency range of 40 MHz to 2 GHz. Restricting the frequency range should ensure that the electrically small assumption implied with the lumped element equivalent circuit is valid. An optimisation procedure, with equivalent circuit R and C as variables, was used to minimise the difference between the simulated and measured S-parameters. Once the equivalent R and C are known, the device Q can be calculated using $Q = \frac{1}{\omega RC}$.

These results indicate that the Pt groundplane leads to a much higher capacitance than devices which do not have a Pt bottom layer [6]. These earlier devices were of a similar size, with the BST layer deposited directly on to a single-crystal LaAlO$_3$ substrate. At 0 V bias, these devices had a capacitance of $\sim$4 pF at 10 GHz. Therefore, a metal groundplane may be useful as a means for reducing the dimensions of BST-based devices.

The relative permittivity $\varepsilon_r$ of the dielectric was determined using a full wave electromagnetic simulation in Ansoft HFSS. To simplify the EM simulation, only half of the device, or one set of interdigital electrodes, was simulated. From the results in Table II, the maximum and minimum capacitances of this structure should be 99.4 pF and 78.3 pF, respectively. Layer thicknesses measured during the fabrication process were used in the EM model. Sweeping the $\varepsilon_r$ of the BST dielectric layer from 75 to 120 in the simulation, the capacitance extracted from the HFSS model varied from 68.9 pF to 107 pF. Fitting the measured data to this curve, the maximum and minimum $\varepsilon_r$ were 110.3 and 88, respectively. The dielectric tunability
can be defined as

$$\text{Tunability} = \frac{\epsilon_{r,\text{max}} - \epsilon_{r,\text{min}}}{\epsilon_{r,\text{max}}}. \quad (1)$$

The dielectric tunability, according to (1), is \(\sim 20\%\). By a similar method, the capacitance tunability is \(\sim 21\%\). This is at the lower end of the range of values reported in the literature [13], which range from 7 to 75 %. However, by reducing the device dimensions and increasing the bias field, it should be possible to produce devices with competitive performance well into the microwave frequency range.

IV. CONCLUSION

A process for the fabrication of BST thin-film varactors has been developed and demonstrated. The device S-parameters have been measured from 40 MHz to 20 GHz. From this data, the capacitance and tunability from 40 MHz to 2 GHz have been extracted, and the results were compared with those of other research groups. A problem with cracking in the Pt film has been identified, and further work is required to address the problem. Efforts are also underway to characterise the electrical properties of the BST film as a function of the sputtering gas pressure.

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REFERENCES


