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Low temperature deposition of high response piezoelectric thin films

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ABSTRACT
Strontium-doping of the popular lead zirconate titanate results in improved piezoelectric response characteristics. We demonstrate the synthesis of these thin films at low temperature of 300 ºC on silicon substrates. High response with an estimated piezoelectric coefficient ($d_{33}$) value of 892 pm/V was measured. Microstructural characterisation results showing strong c-axis texture and lattice guiding effects are also presented.

KEYWORDS
thin films; electro-mechanical properties; crystalline oxides; high-resolution electron microscopy; nanoindentation

PACS INDEXING CODES
77.65.Bn, 73.40.Rw, 81.70.Bt, 77.55.fg, 68.37.Lp, 68.35.Fx
Lead zirconate titanate (PZT) thin films are reputed for their relatively high piezoelectric performance [1,2], based on value of the piezoelectric response coefficients such as the piezoelectric charge coefficient $d_{33}$. Reported values of $d_{33}$ for PZT thin films vary from ~100-400 pm/V. This relatively high response, often unmatched by other piezoelectric materials has led to the widespread use of PZT thin films in electronic devices for sensing and actuation, but often limited in processing capabilities by the high deposition temperature required (600-700 °C).

The ability to successfully synthesise high performance piezoelectric thin films at lower temperatures provides the capability for more versatile integration with microelectronics, expanding the range of potential applications. Towards achieving this objective, the theoretical basis, synthesis, and characterisation of low temperature deposited, high piezoelectric response thin films are described in this article.

While PZT displays excellent piezoelectric response, it has been found that substituting strontium for a small percentage (about 1.6 %) of lead atoms at the ‘A’-site of the perovskite $ABO_3$ structure of PZT enhances the piezoelectric response [3-7]. Thin films of PSZT (1.6 μm thick) were deposited on metal-coated silicon substrates by RF magnetron sputtering under the conditions given in Table 1. The silicon (100) substrates were first dipped in hydrofluoric acid to remove the native oxide layer. A diffusion barrier of 200 nm silicon dioxide was deposited, following which the gold electrode layer of 150 nm thickness was coated with the aid of a 15 nm titanium adhesion layer. All three layers were sequentially deposited by electron beam evaporation without breaking vacuum (at room temperature and under vacuum of $1 \times 10^{-7}$ Torr). Following this, sputtering was carried out, with the samples placed on a 3-inch resistive substrate heater, which was compatible with deposition in an oxygen atmosphere [8]. Very accurate control of temperature was achieved using an Eurotherm Controls, Inc. Model 808 temperature controller programmer. The post-deposition cooling rate was found to influence the degree of perovskite orientation in the thin films [9] based on which a cooling rate of 5 °C/min was chosen. The deposited thin films were extensively characterised using a combination of X-ray diffraction (under conditions as in Ref. [10]) and transmission electron microscopy (under conditions described in Refs. [11] and [12]).
Piezoelectric response measurements on PSZT thin films samples were carried out using a nanoindenter (Fig. 1) to estimate the piezoelectric charge coefficient $d_{33}$, using the technique described in Ref. [7]. This technique was rigorously tested during development to ensure that the value obtained were quantitatively accurate, by ascertaining that similar values were obtained using both the nanoindenter and an atomic force microscope under the same testing arrangement [7,13]. Control samples comprising of amorphous silica thin films (no PSZT) were tested demonstrating no response under an applied electric field. Moreover, substrate bending did not influence the results obtained, as no force was applied on the films during testing – identical values were obtained for films deposited on silicon and alumina substrates. The samples were studied under the influence of the inverse piezoelectric effect, by applying an electric field and observing the variation in the film thickness. More than one top electrode was defined on samples of interest to study the uniformity of the piezoelectric response in different regions of the samples. The response variations (as peak-to-peak changes in film thickness) over an 8 μm × 8 μm region under an applied electric peak-to-peak voltage of 32.8 V for a 1.6 μm thick PSZT thin film sample is shown in Fig. 2. This figure shows bands of piezoelectric response variations which corresponded to a minimum peak-to-peak thickness variation of 24.0 nm and a maximum of 29.3 nm, which respectively correspond to $d_{33}$ values of 732 pm/V and 892 pm/V. These piezoelectric response values were extracted from the raw data comprising of sinusoidal variations of the film thickness, averaging the measurements over five cycles.

This ultra-high value of $d_{33}$ (a maximum of 892 pm/V), which is more than two times higher than the maximum value reported for PZT thin films on silicon (419 pm/V) [14], is an improvement of 50% (one and a half times) on the highest thin film $d_{33}$ value of 608 pm/V reported previously by us for PSZT thin films on gold [7]. The ultra-high piezoelectric response measured for these PSZT thin films can be attributed to three factors. Firstly, the PSZT thin films were deposited under optimised conditions following extensive analysis [8,9,12]. Secondly, the inclusion of the silicon dioxide layer improved the degree of preferential orientation in the PSZT thin films (Fig. 3), apparently caused by the increased guiding effect from the underlying gold layer (due to suppression of amorphous layer
formation and enhanced gold texturing). Finally, and most importantly, the piezoelectric response observed could be attributed to the modified (and expanded) unit cell of the PSZT thin films under study [10]. This larger unit cell creates more room for atomic displacements under an applied electric field [15], with the capability of causing higher levels of strain in the PSZT thin films.

The values of $d_{33}$ reported are termed ‘effective values’, as these apply for piezoelectric response measurements carried out on continuous thin films which are two-dimensional (length and width dimensions much greater than the film thickness). Lefki and Dormans [16] and Nagarajan et al. [17] have shown that the piezoelectric response of such films are damped by substrate clamping effects and in reality would have a component of error in the results. However, for thin film applications, the results presented in this article are very relevant.

The only comparable result demonstrating ultra-high piezoelectric response thin films with $d_{33}$ values up to 2000 pm/V was reported by Ouyang et al. [18]. While this result clearly demonstrates the ability to engineer films to attain high piezoelectric response, this deposition was carried out at 600 ºC on SrTiO$_3$ substrates. We demonstrate comparable high piezoelectric response for deposition at 300 ºC on metal-coated silicon substrates. This makes the process compatible with microsystems fabrication using widely accepted silicon technology.

In summary, this article presents results for the piezoelectric response characterisation of optimised low temperature piezoelectric thin films on silicon substrates. High response with a maximum $d_{33}$ value of 892 pm/V was measured, for the PSZT thin films deposited on metallised silicon substrates at 300 ºC. The origin of the ultra-high response can be attributed to optimised deposition conditions, the pronounced preferential texture in the thin films, and a modified unit cell structure.
ACKNOWLEDGEMENTS

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REFERENCES


Table 1. RF magnetron sputtering conditions for PSZT.

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<tr>
<td>Target</td>
<td>((\text{Pb}<em>{0.92}\text{Sr}</em>{0.08})(\text{Zr}<em>{0.65}\text{Ti}</em>{0.35})\text{O}_3)</td>
</tr>
<tr>
<td>Target diameter</td>
<td>100 mm</td>
</tr>
<tr>
<td>RF power</td>
<td>100 W</td>
</tr>
<tr>
<td>Target to substrate distance</td>
<td>70 mm</td>
</tr>
<tr>
<td>Process gas</td>
<td>10% oxygen in argon</td>
</tr>
<tr>
<td>Base pressure</td>
<td>(9.0 \times 10^{-6}) Torr</td>
</tr>
<tr>
<td>Sputtering pressure</td>
<td>(1.0 \times 10^{-2}) Torr</td>
</tr>
<tr>
<td>Substrate temperature</td>
<td>300 °C</td>
</tr>
<tr>
<td>Temperature ramp-up rate</td>
<td>10 °C/min</td>
</tr>
<tr>
<td>Temperature ramp-down rate</td>
<td>5 °C/min</td>
</tr>
<tr>
<td>Sputtering duration</td>
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FIGURE CAPTIONS

FIG. 1. Schematic representation of the nanoindentation-based piezoelectric response measurement arrangement.

FIG. 2. Result from mapping the piezoelectric response over an 8 μm × 8 μm area on the PSZT thin film surface.

FIG. 3. Characterisation results for the PSZT thin film samples: (a) X-ray diffractogram showing the strong preferential orientation of PSZT (104) and Au (111); (b) High resolution transmission electron micrograph showing the guiding effect of Au (111) on PSZT (104).
Figure 1

Nanoindenter tip

Top electrode

PSZT

Bottom electrode

Adhesion layer(s)

(100) Silicon substrate

\[ V(t) = V_p \times f(t) \]

Ground
Figure 2

Change in thickness (nm):
- 24-25
- 25-26
- 26-27
- 27-28
- 28-29
- 29-30